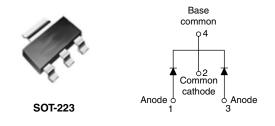


Vishay High Power Products

Schottky Rectifier, 2 x 1 A



PRODUCT SUMMARY		
I _{F(AV)}	2 x 1 A	
V _R	60 V	

FEATURES

- Small foot print, surface mountable
- Low profile
- Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Common cathode
- Designed and qualified for industrial level

DESCRIPTION

The 20CJQ060 surface mount Schottky rectifier series has been designed for applications requiring very low forward drop and very small foot prints. Typical applications are in portables, switching power supplies, converters, automotive system, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	2	А	
V _{RRM}		60	V	
I _{FSM}	t _p = 5 μs sine	385	A	
V _F	1 Apk, $T_J = 125 \ ^\circ C$ (per leg)	0.56	V	
TJ	Range	- 55 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	20CJQ060	UNITS	
DC reverse voltage	V _R 60		V	
Working peak reverse voltage	V _{RWM}	00	v	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per leg	Iran	50 % duty cycle at T _C = 127 °C, rectangular waveform		1	
See fig. 5	per device	IF(AV)	50 % duty cycle at T _C = 109 °C, rectangular waveform		2	А
Maximum peak one cycle		l	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	385	A
non-repetitive surge current per leg See fig. 7		IFSM	10 ms sine or 6 ms rect. pulse	V_{RRM} applied	22	
Non-repetitive avalanche energy per leg E _{AS}		E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 3 mH		1.5	mJ
Repetitive avalanche curre	ent per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _B typical		1.0	А

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.59	v
		2 A		0.75	
		1 A	T _J = 125 °C	0.56	
		2 A		0.67	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_R = Rated V_R$	0.1	mA
See fig. 2		T _J = 125 °C		5.0	
Typical junction capacitance per leg	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		60	pF
Typical series inductance per leg	LS	Measured lead to lead 5 mm from package body		6	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_{J} ⁽¹⁾ , T_{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to ambient	R _{thJL}	DC operation	25	°C/W
Maximum thermal resistance, junction to lead	R _{thJA}		65	
Approximate weight			0.13	g
		0.0045	OZ.	
Marking device		Case style SOT-223	2CJ	QH

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



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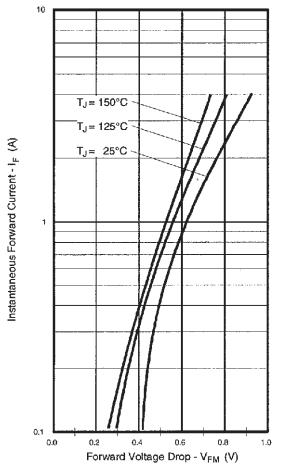
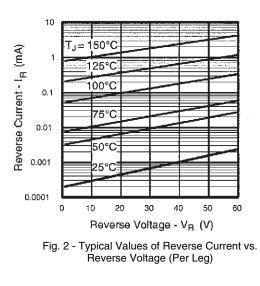


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)



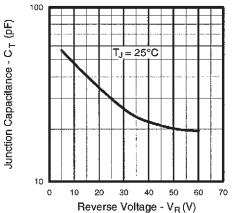


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

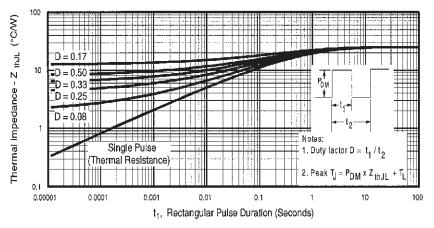
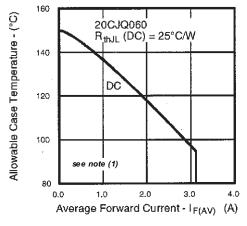


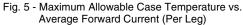
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

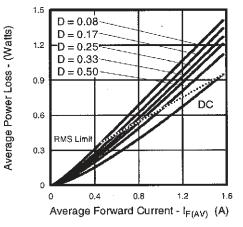
20CJQ060

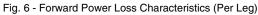
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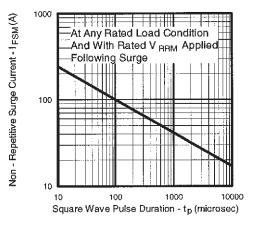


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

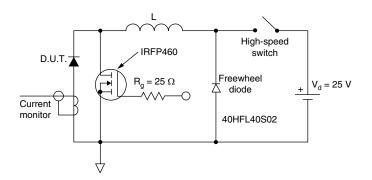


Fig. 8 - Unclamped Inductive Test Circuit

Note

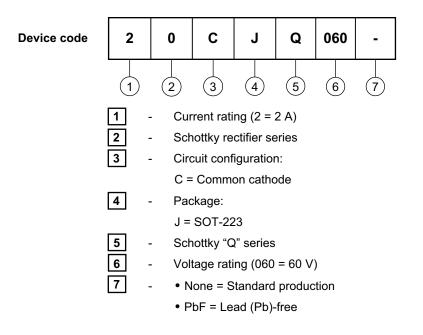
(1)

 $\begin{array}{l} \mbox{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{th,JC}; \\ Pd = \mbox{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ fig. \ 6); \\ Pd_{REV} = \ Inverse \ power \ loss = V_{R1} \ x \ I_R \ (1 - D); \ I_R \ at \ V_{R1} = 80 \ \% \ rated \ V_R \end{array}$



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95022			
Part marking information http://www.vishay.com/doc?95031			
Packaging information http://www.vishay.com/doc?95035			



Vishay

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